Electronic Pater	t App	olication Fe	e Transm	ittal			
Application Number:	10	10796502					
Filing Date:	08	08-Mar-2004					
Title of Invention:	Sy	Systems and methods for reducing static and total power consumption in programmable logic device architectures					
First Named Inventor/Applicant Name:	Da	David Mendel					
Filer:	Je	Jeffrey Howard Ingerman					
Attorney Docket Number:	00	000174-0308-101					
Filed as Large Entity							
Utility Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:					1		
Pages:							
Claims:		12.7.0	3.01				
Independent claims in excess of 3	-	1201	3	200	600		
Miscellaneous-Filing:			'				
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Tota	600		